

2nd Workshop on  
**APPLIED SIGNAL PROCESSING**

22TH October 2015  
ESTIG AUDITORIUM I  
14H00-17H00

**Program**

**Henrique Oliveira** (Lab Responsible)  
ESTIG-IPBeja  
PhD Electrical Engineering and Computers  
*Welcome and Lab Presentation*  
14h00m-14h10m (10 min)

**Marek Kubalčík**  
Tomas Bata University in Zlín  
Faculty of Applied Informatics  
*Predictive Control with Filtering of Input and Output Variables*  
14h10m-14h40m (30 min)

**José Caeiro**  
ESTIG-IPBeja  
PhD Electrical and Computer Engineering  
*Signal and Image Processing with Python Tools (including Heterogeneous Programming with OpenCL)*  
14h50m-15h10m (30 min)

**Coffee Break (20 min)**

**Milan Adámek**  
Tomas Bata University in Zlín  
Faculty of Applied Informatics  
*Modelling and Design of Time-of-Flight Sensor*  
15h40m-16h10m (30 min)

**João Martins**  
ESTIG-IPBeja  
PhD Electrical and Computer Engineering  
*The TI Microcontrollers Ecosystem: Examples and Applications*  
16h10m-16h30m (20 min)

**Marek Kubalčík**  
Tomas Bata University in Zlín  
Faculty of Applied Informatics  
*A Survey of Research Fields at the Faculty of Applied Informatics, TBU*  
16h30m-16h50m (20 min)

The workshop is organized by the **LabSEPSI** research laboratory from ESTIG-IPBeja. The attendance is free and every participant will be given a presence certificate when requested.